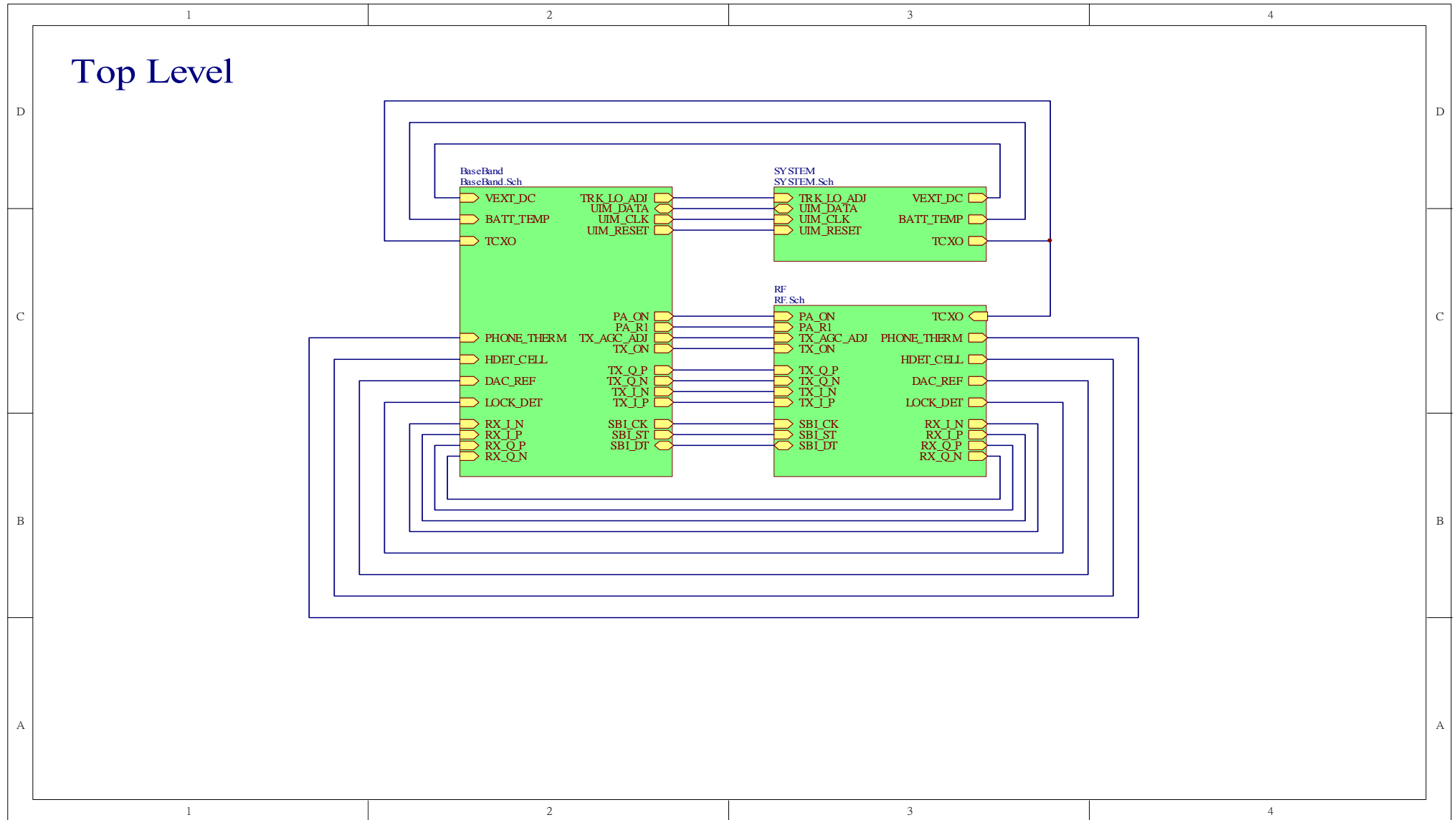
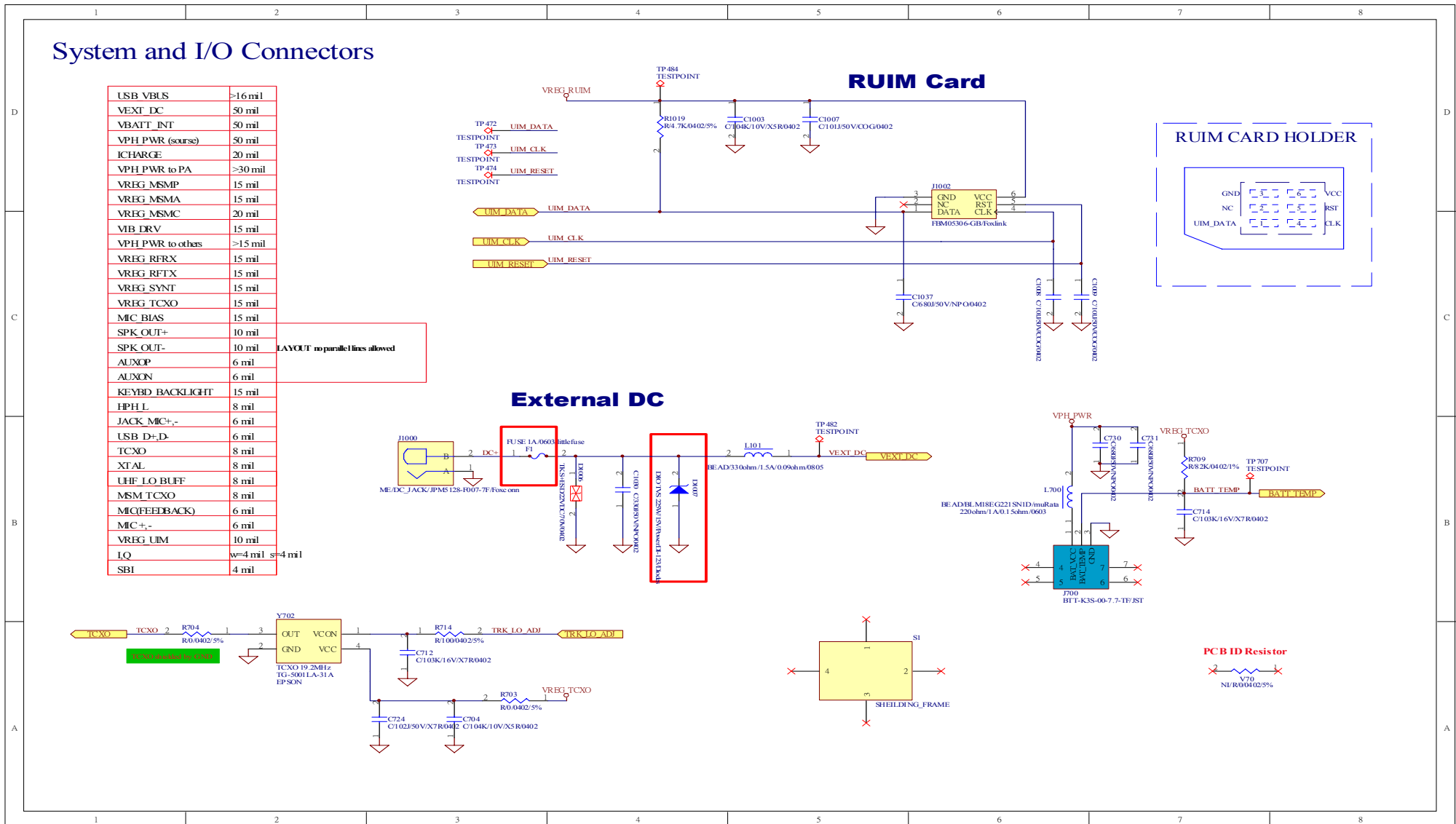


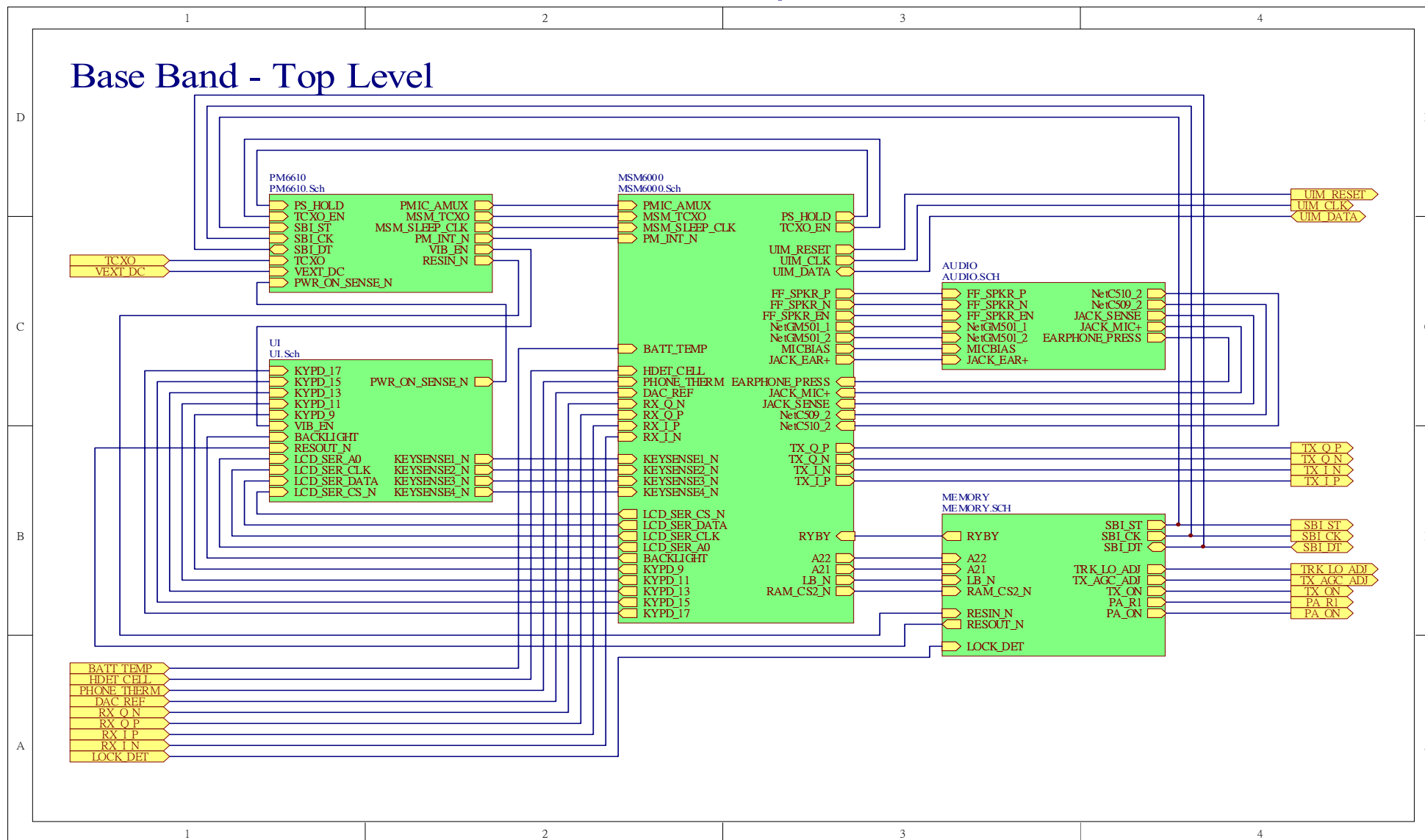
Top Level



System and I/O Connectors



Base Band - Top Level



PM6610

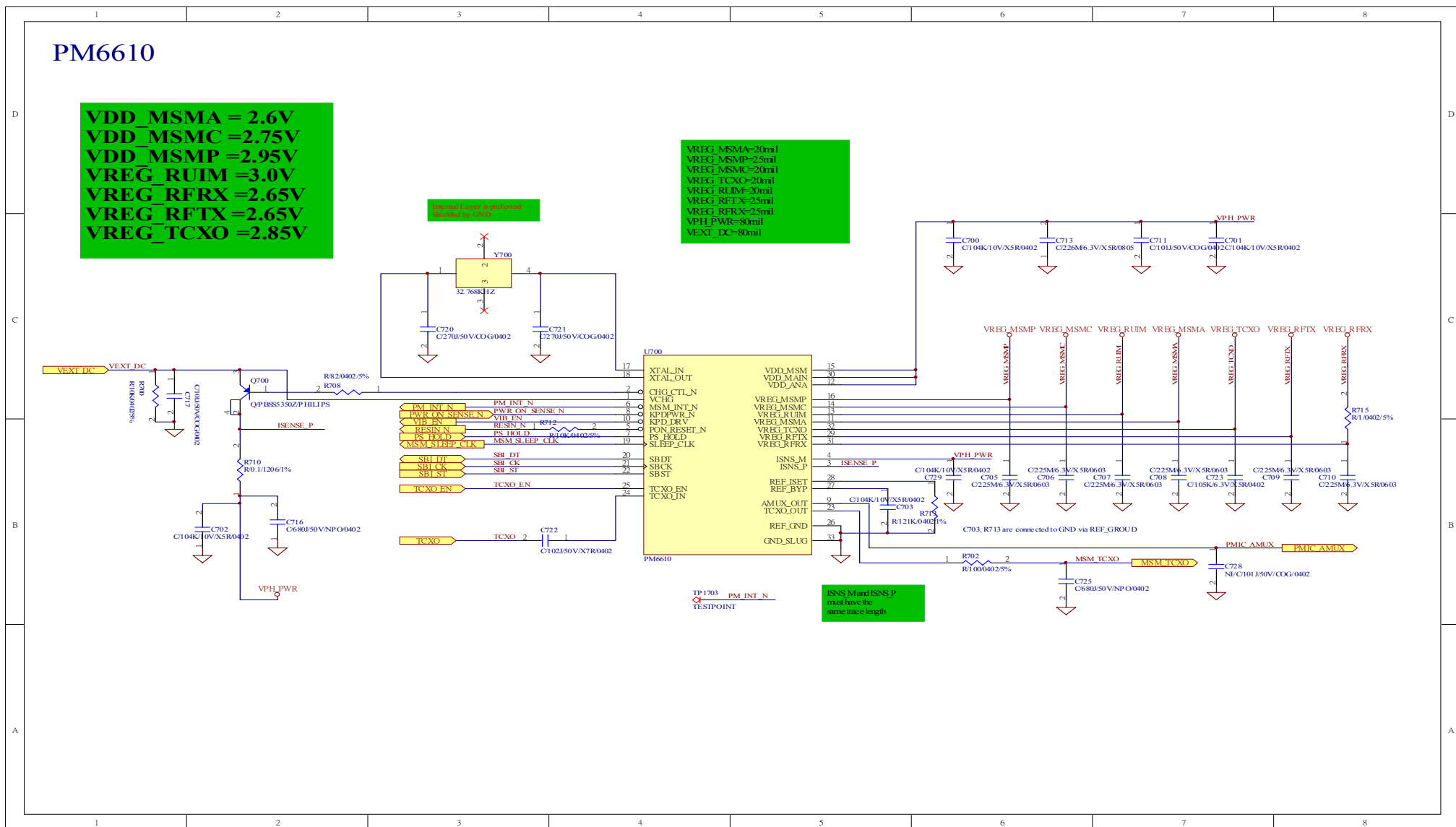
PM6610

VDD_MSMA = 2.6V
 VDD_MSMP = 2.75V
 VDD_MSMP = 2.95V
 VREG_RUIM = 3.0V
 VREG_RFRX = 2.65V
 VREG_RFTX = 2.65V
 VREG_TCXO = 2.85V

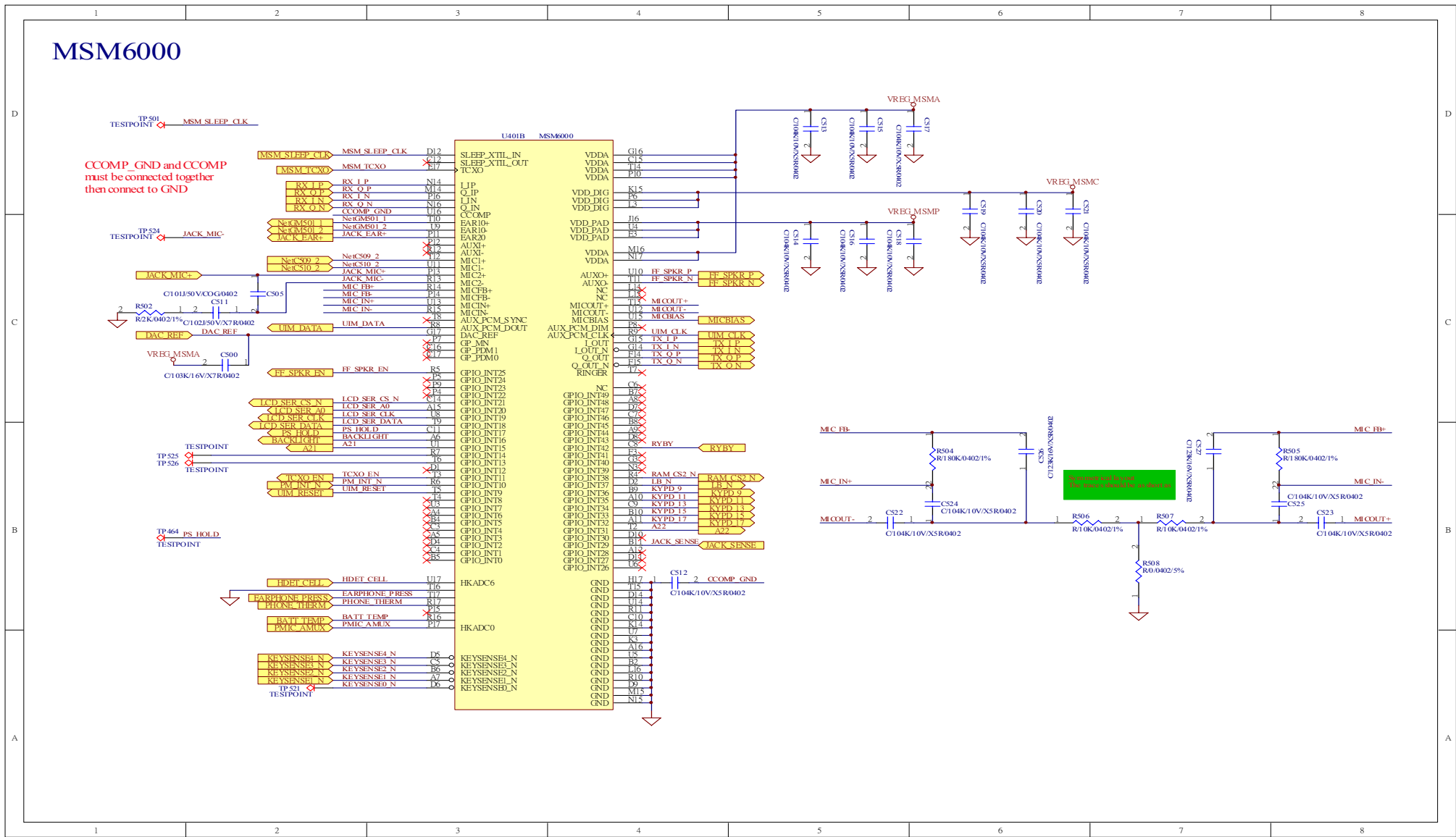
VREG_MSMA=20nF
 VREG_MSMP=25nF
 VREG_MSMP=20nF
 VREG_TCXO=20nF
 VREG_RUIM=20nF
 VREG_RFTX=25nF
 VREG_RFRX=25nF
 VPH_PWR=80nF
 VEXT_DC=80nF

TP1703 PM INT_N must have the same trace length

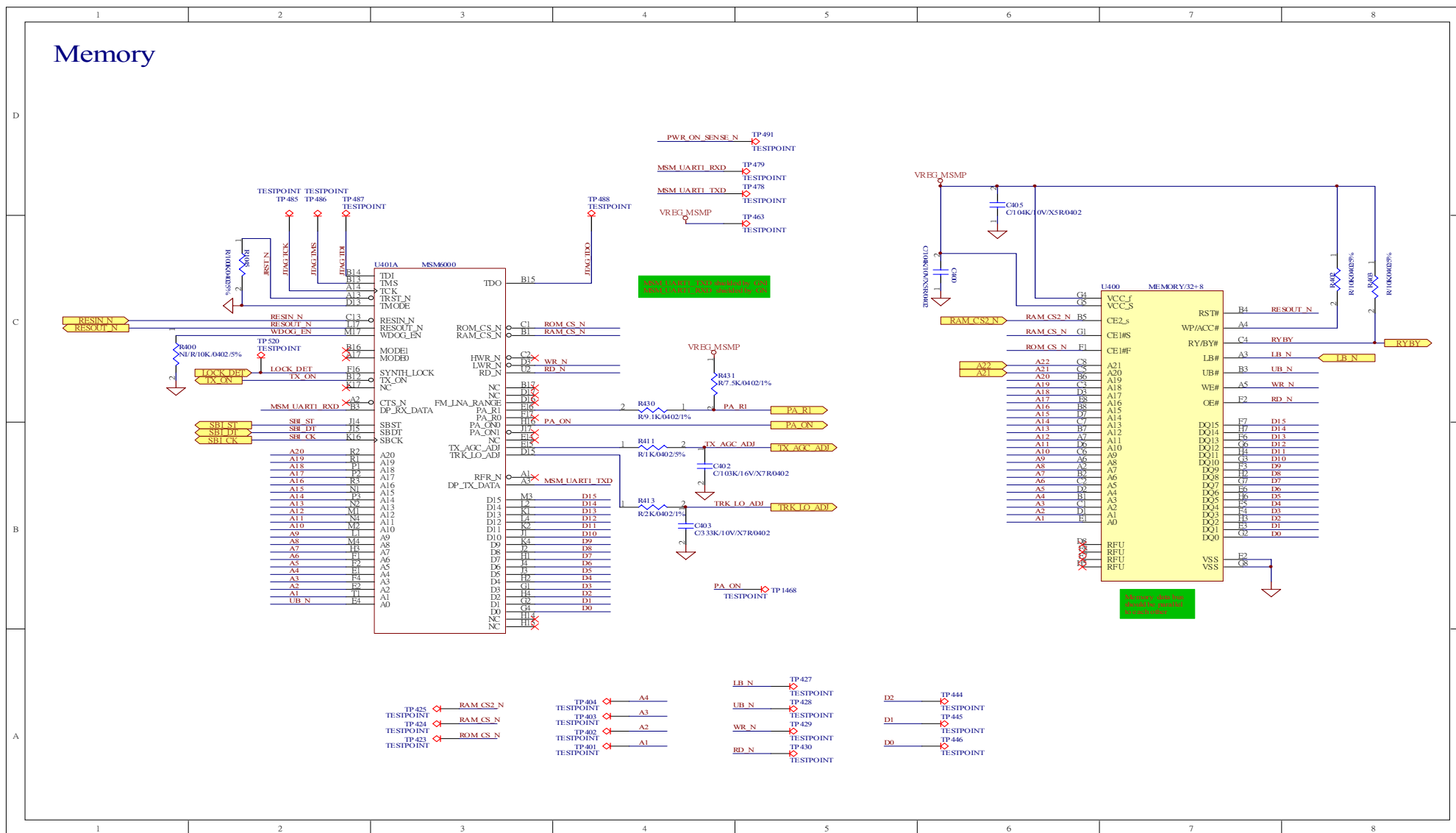
ISNS_M and ISNS_P must have the same trace length



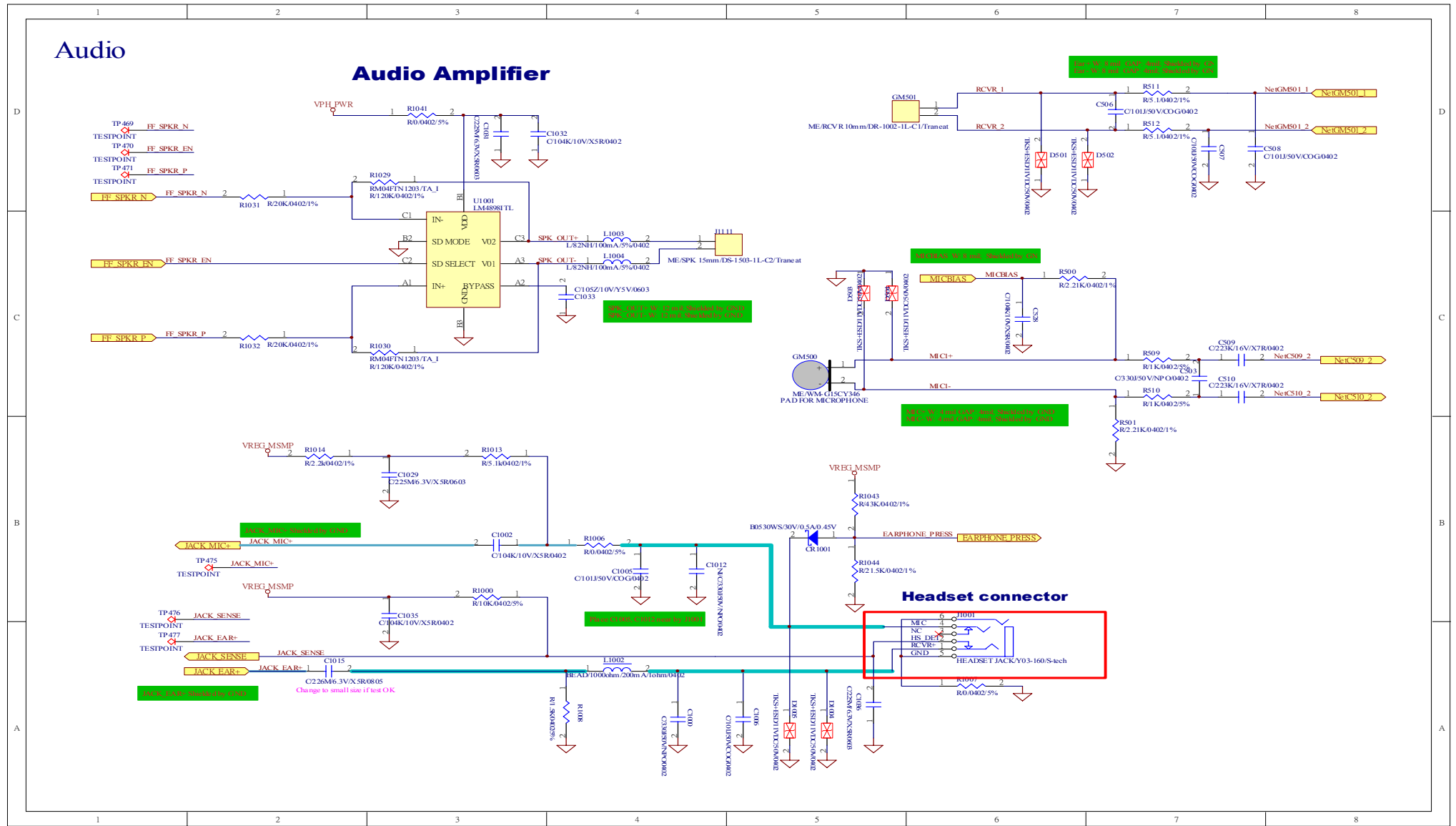
MSM6000



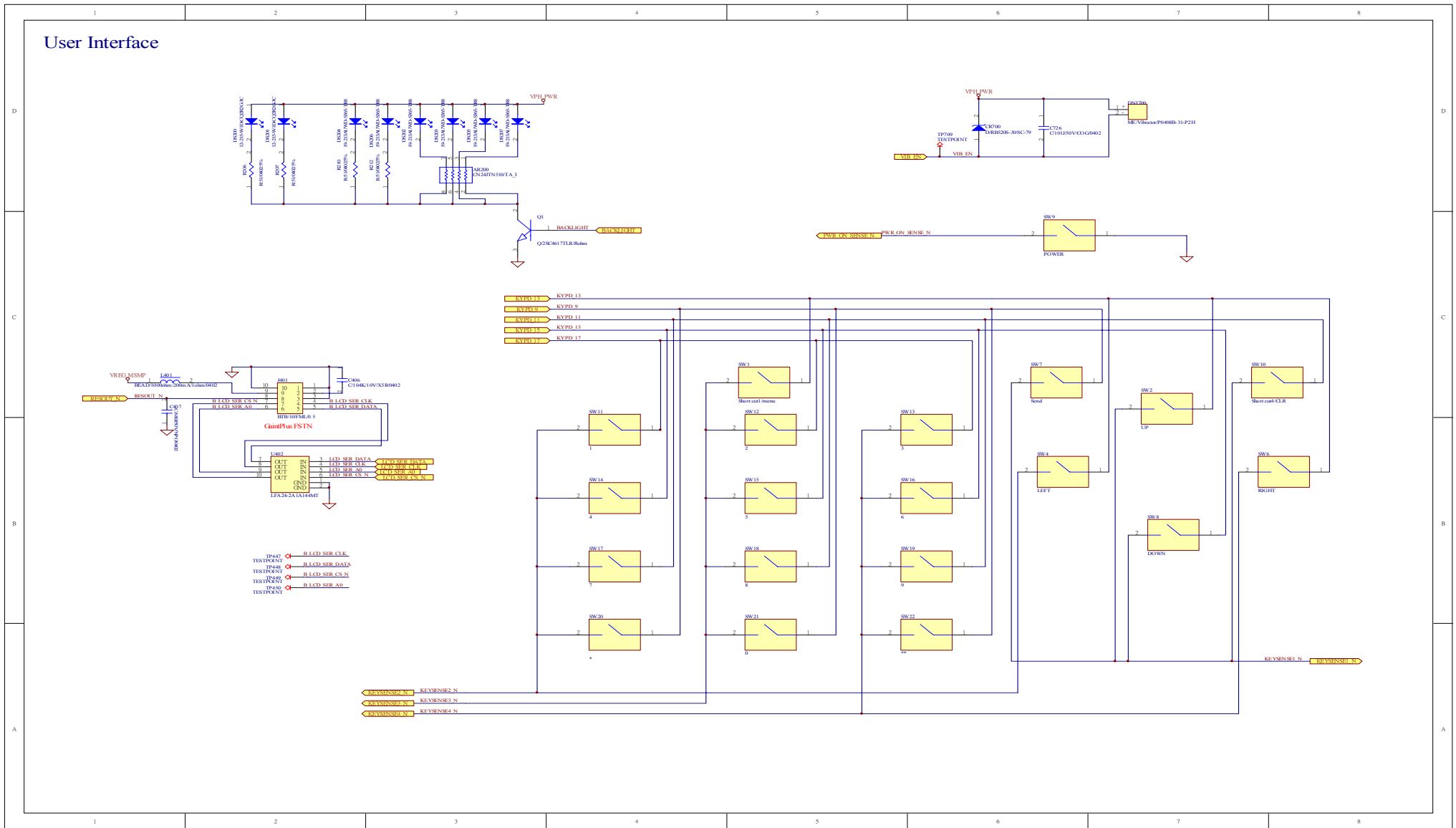
Memory



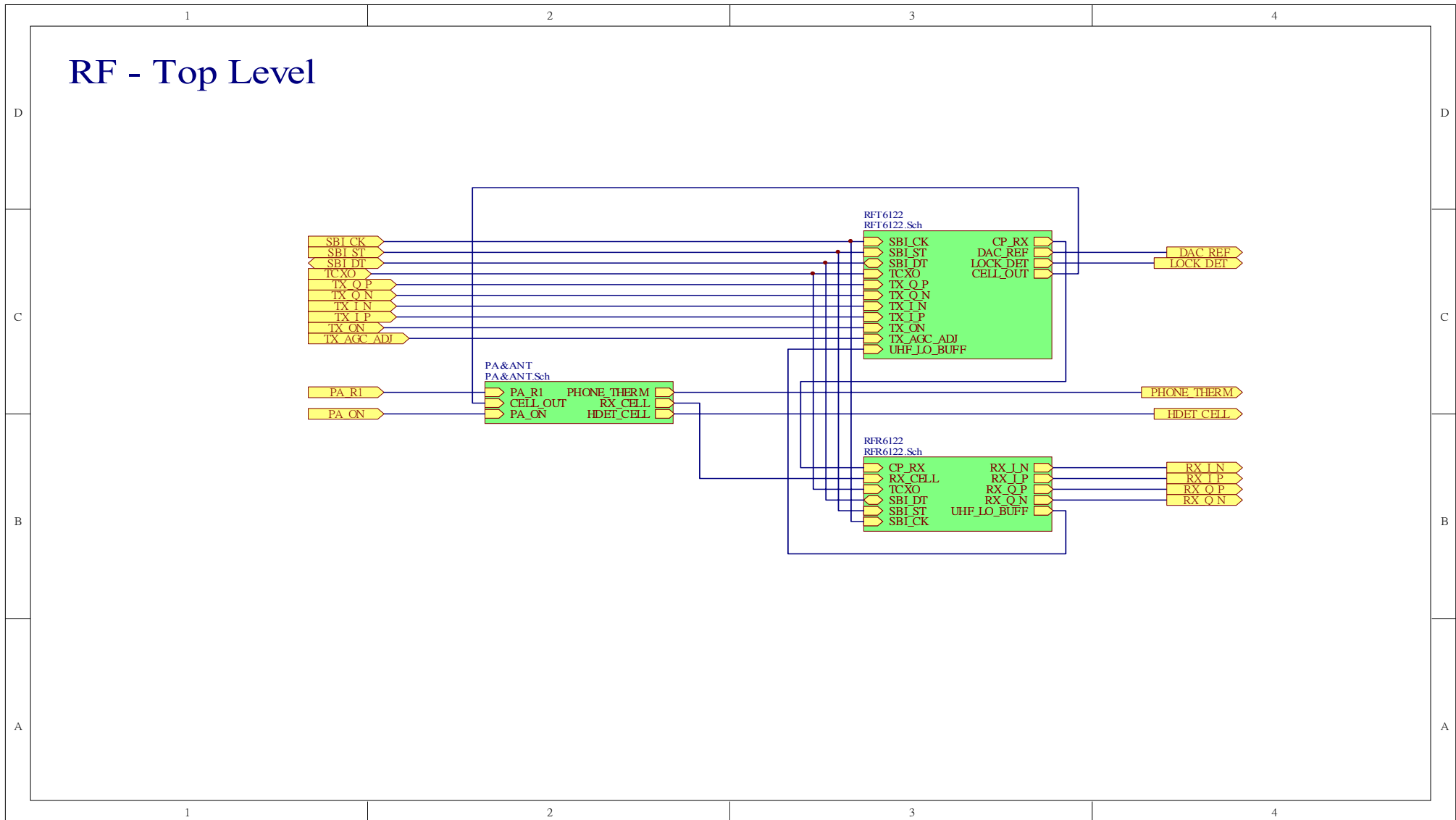
Audio



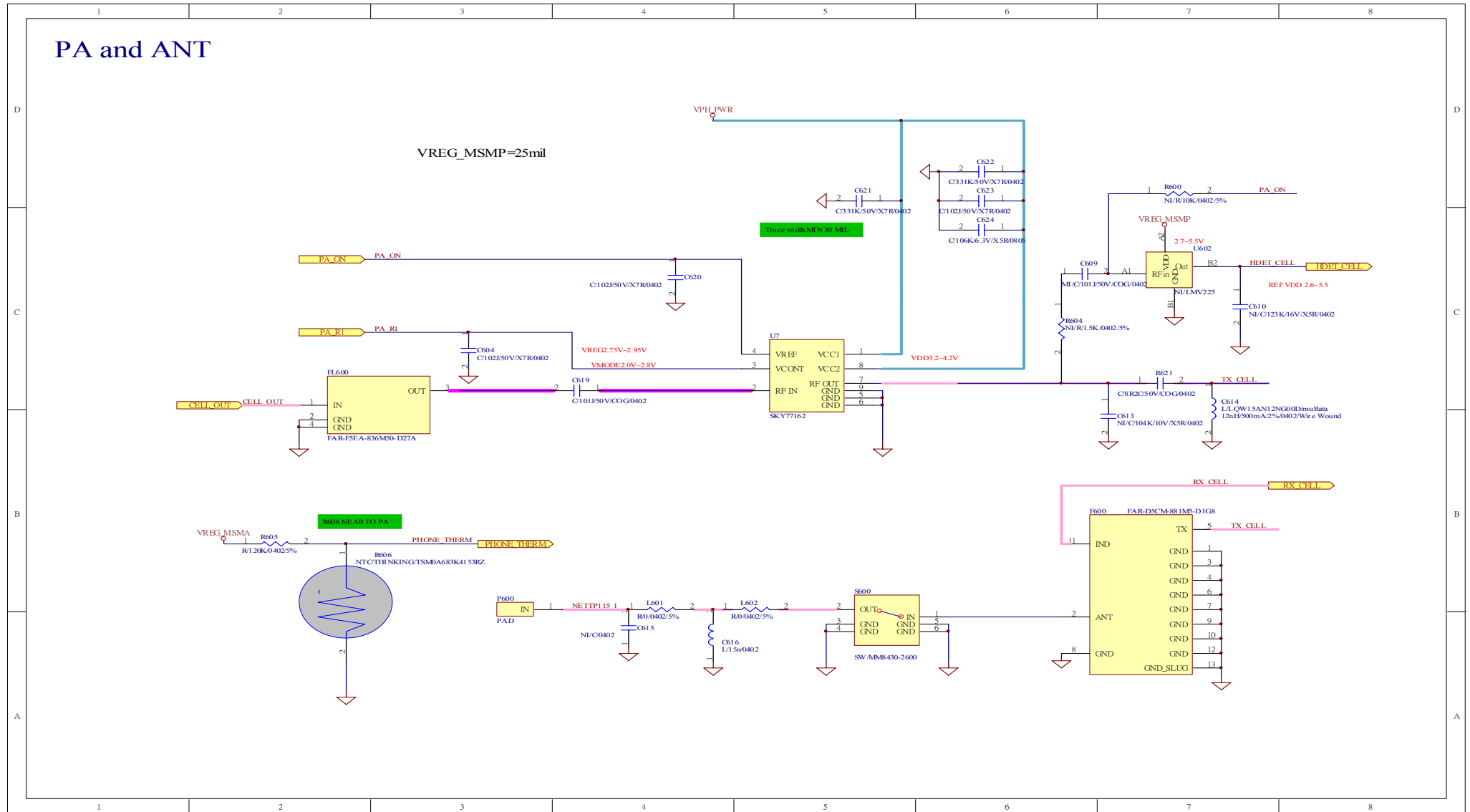
User Interface



RF - Top Level

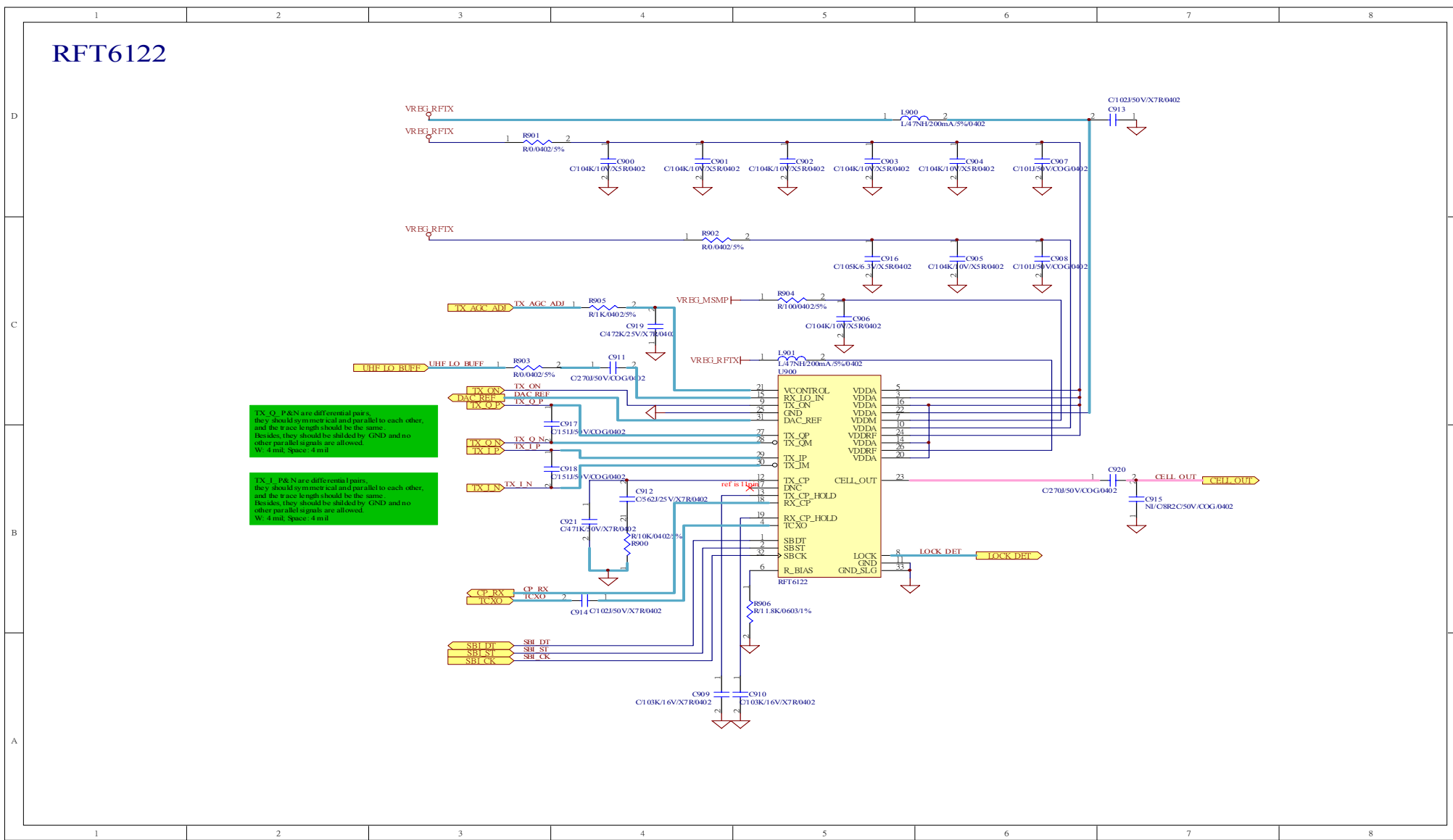


PA and ANT

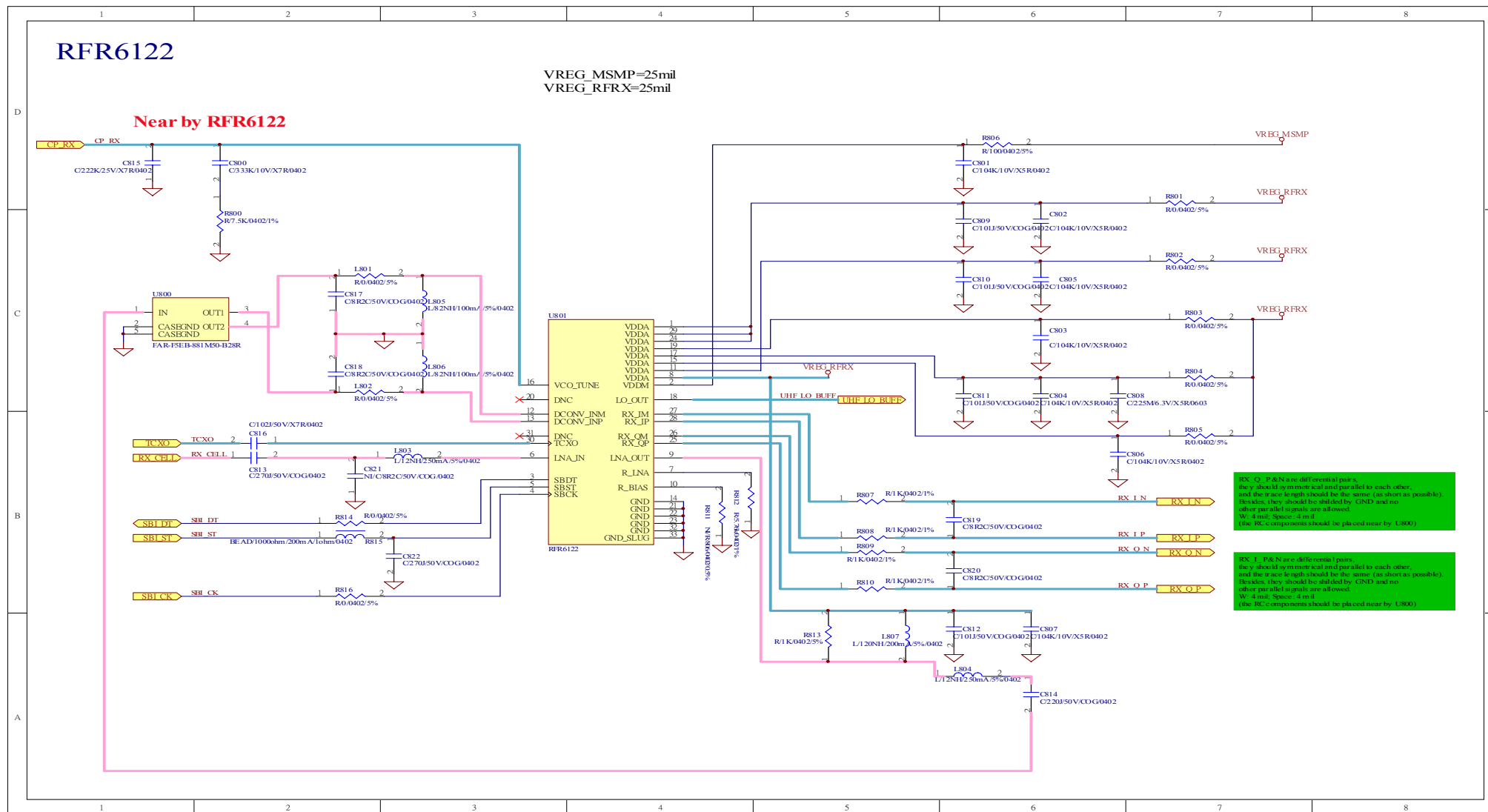


RFT6122

RFT6122



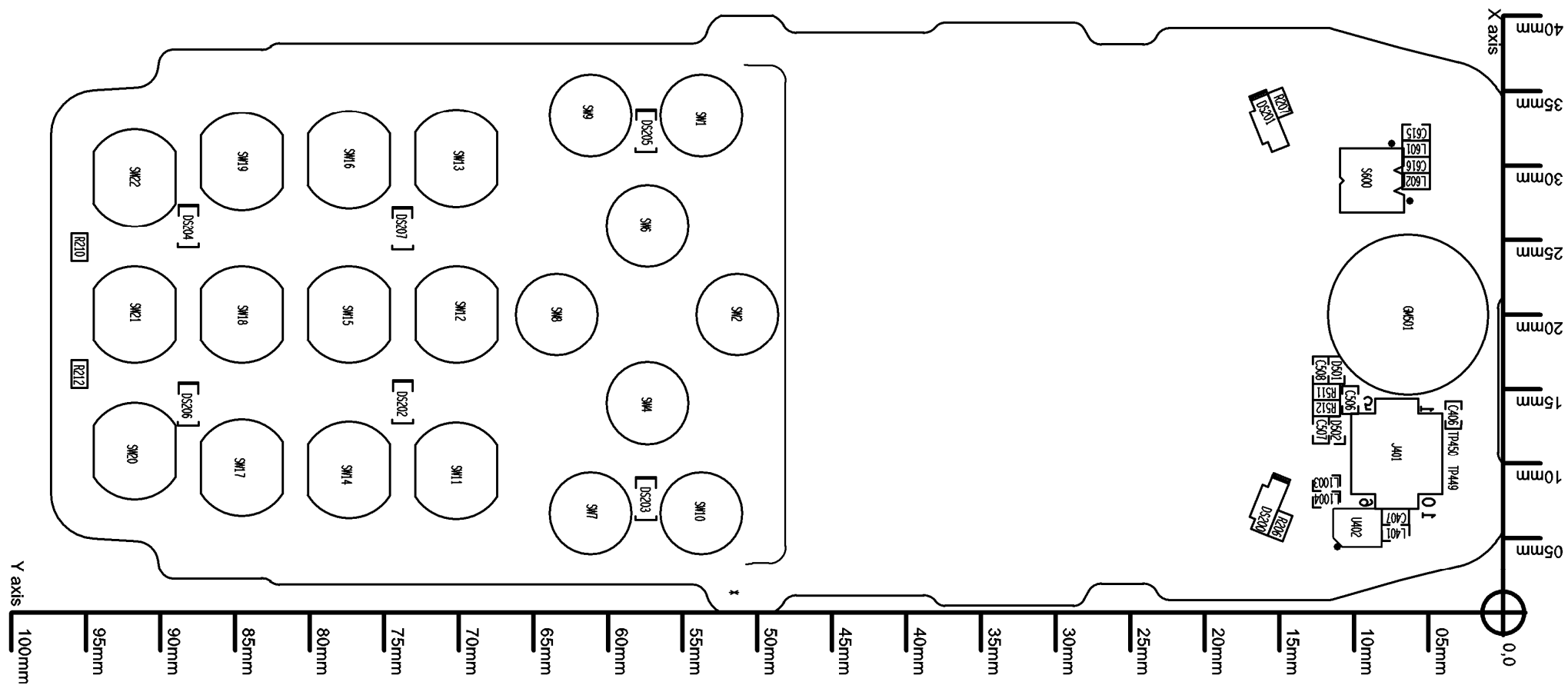
RFR6122



RX_I_N, P&N are differential pairs, they should symmetrical and parallel to each other, and the trace length should be the same (as short as possible). Besides, they should be shielded by GND and no other parallel signals are allowed. W: 4mil, Space: 4mil (the RC components should be placed near by U800)

RX_O_N, P&N are differential pairs, they should symmetrical and parallel to each other, and the trace length should be the same (as short as possible). Besides, they should be shielded by GND and no other parallel signals are allowed. W: 4mil, Space: 4mil (the RC components should be placed near by U800)

1255 (RH-79) Component Layout - Top



1255 (RH-79) Component Layout - Bottom

